Solutions to Chapter 5:

Exercise 5.1: Production of c-Si Solar Cells

- a) See Section 5.1
- b) 1. Texturizing,
 - 2. n^+ -doping of the emitter
 - 3. Anti-reflection coating
 - 4. Screen printing of contacts (front and rear side)
 - 5. Contact firing
 - 6. Edge insulation
 - 7. Measurement and classification of cell

Exercise 5.2: a-Si Thin Film Cells

- a) The drift cell is built up of a very thin pin-layer with inbuilt electrical field. The generated charge carriers are separated by the field and moved to the contacts. On the contrary the diffusion cell is a (e.g.) p-doped wafer with a thickness larger than 100 μ m in which uppermost layer an n-emitter has been doped. The light absorption mainly takes place in the field-free base so that the generated particles must first diffuse to the p-n junction to be afterwards transported to their home regions by the field of the space charge region.
- b) See Section 5.2.3
- c) See Section 5.2.4
 Moderation of the effect: Passivate with hydrogen
 Use thisner layers (in tendem or triple cell
 - Use thinner layers (in tandem or triple cells)
 - Temper to heal the degradation

Exercise 5.3: CIS Cells

- a) See Figure 5.22
- b) It serves on the one hand as an n-layer of the p-n junction and on the other hand as a window layer. This means that it has a large bandgap and will therefore absorb almost no photons.
- c) The standard buildup is the superstrate configuration (*super*: Latin for *above*): Here the glass pane lies above and the cell is illuminated through the glass. Advantageous to this solution is the fact that the glass pane can be the cover of the whole solar module.

For the substrate configuration (*sub*: Latin for *under*) the glass pane - in a manner of speaking - lies under the cell; light incidence comes from the other cell side. This structure is necessary when the absorber only can be deposited on a lightproof material (e.g. molybdenum in CIS cells). In this case an additional module front glass pane has to be used on the other side.

Exercise 5.4: Concentrator Systems

- a) See Figure 5.25
- b) The short circuit rises linearly with an increase of the incident irradiance. Simultaneously V_{MPP} rises so that P_{MPP} increases over-proportional.

c)
$$V'_{OC} = m \cdot V_{T} \cdot \ln \frac{X \cdot I_{SC}}{I_{S}} = m \cdot V_{T} \cdot \ln \frac{I_{SC}}{I_{S}} + m \cdot V_{T} \cdot \ln X = V_{OC} + m \cdot V_{T} \cdot \ln X$$

 $\frac{\Delta V_{OC}}{V_{OC}} = \frac{V'_{OC} - V_{OC}}{V_{OC}} = \frac{m \cdot V_{T} \cdot \ln X}{V_{OC}}$
i) $X_{1} = 100$: $\frac{\Delta V_{OC}}{V_{OC}} = \frac{15 \cdot 26 \text{ mV} \cdot \ln 100}{600 \text{ mV}} = 0.3 \Rightarrow \eta_{1} = \eta \cdot (1 + 0.3) = \frac{23.4 \text{ \%}}{23.4 \text{ \%}}$
ii) $X_{2} = 400$: $\frac{\Delta V_{OC}}{V_{OC}} = \frac{1.5 \cdot 26 \text{ mV} \cdot \ln 400}{600 \text{ mV}} = 0.389 \Rightarrow \eta_{1} = \eta \cdot (1 + 0.389) = \frac{25.0 \text{ \%}}{25.0 \text{ \%}}$

d) Heating up of the cell, rising losses at series resistances

Exercise 5.5: Ecology Questions

a) c-Si: no problems
 CdTe: upper limit through restricted availability of tellurium; about 10 GWp/a possible
 CIS: upper limit through restricted availability of indium; about 5 GWp/a possible

b) i)
$$w_{Year} = 900 \text{ kWh/(kWp·a)}, T_A = \frac{w_{Prod}}{w_{Year} \cdot F_{PE}} = \frac{5500 \text{ kWh/kWp}}{900 \frac{\text{kWh}}{\text{kWp·a}} \cdot 3} = \frac{2.04 \text{ a}}{3};$$

$$ERoEI = \frac{T_{\rm L}}{T_{\rm A}} = \frac{25 \text{ a}}{2.04 \text{ a}} = \frac{12.3}{2.04 \text{ a}}$$

ii) With Table 2.4: $w_{Year} = 900 \text{ kWh/(kWp} \cdot a) \cdot 81.9 \% = 737 \text{ kWh/(kWp} \cdot a)$

$$T_{\rm A} = \frac{w_{\rm Prod}}{w_{\rm Year} \cdot F_{\rm PE}} = \frac{5500 \text{ kWh/kWp}}{737 \text{ kWh}} = \frac{2.49 \text{ a}}{\text{kWp} \cdot \text{a}}; \quad ERoEI = \frac{T_{\rm L}}{T_{\rm A}} = \frac{25 \text{ a}}{2.49 \text{ a}} = \underline{10.0}$$